

20.
26.

(Amended once) A memory system comprising:

a bus;

a plurality of stacks of memory devices;

a buffer coupled between the plurality of stacks of memory devices and the bus;

a second plurality of stacks of memory devices; and

a second buffer coupled between the second plurality of stacks of memory devices

and the first buffer.

21.
27.

(Amended once) A memory system comprising:

a bus;

a plurality of stacks of memory devices; and

a buffer coupled between the plurality of stacks of memory devices and the bus;

wherein the buffer is adapted to receive a signal from the bus and redrive the signal to another buffer.